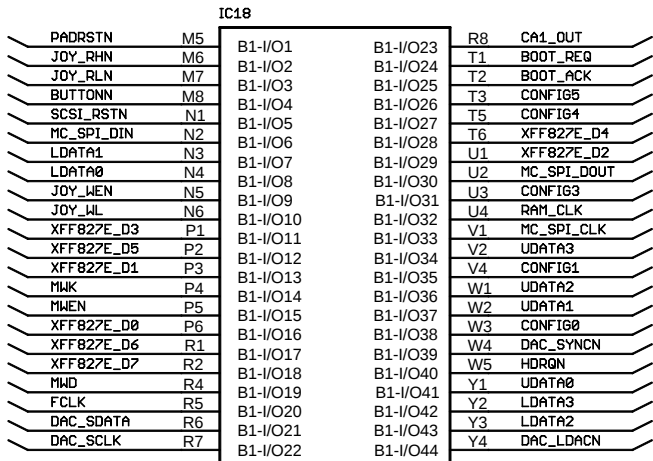
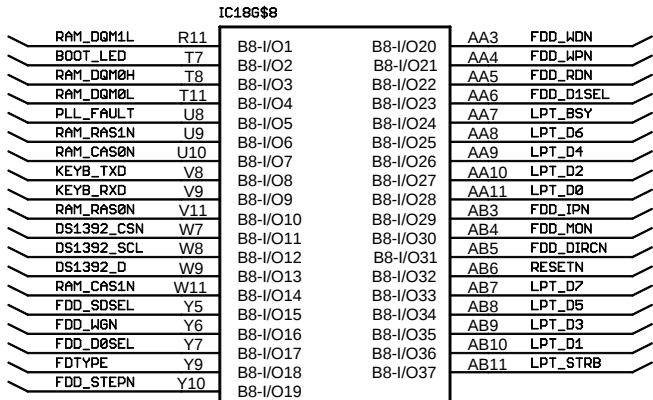
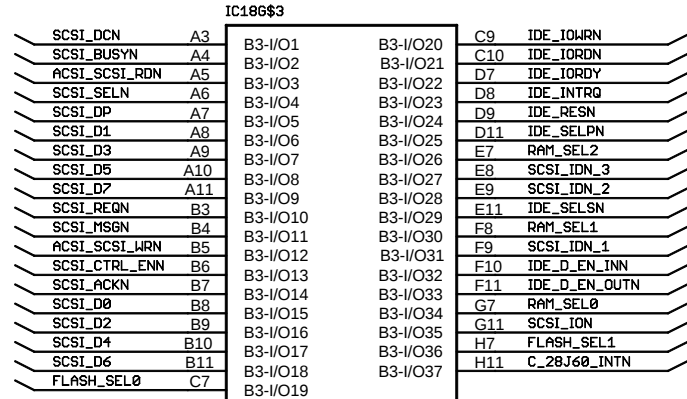




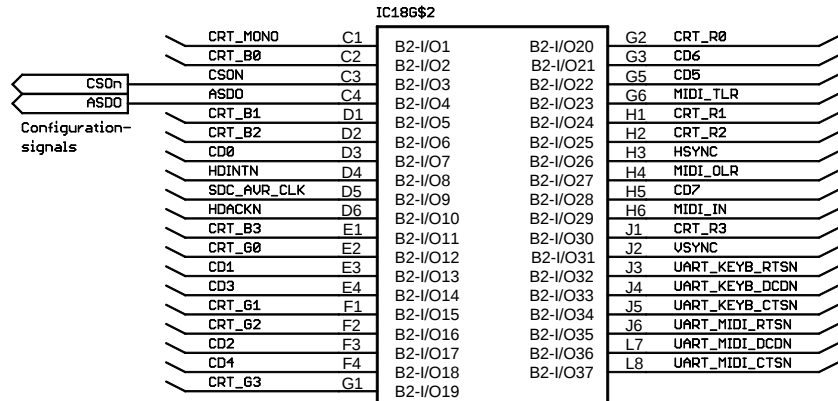
EP2C35F484



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EP2C35F484



EP2C35F484

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FPGA (part1)

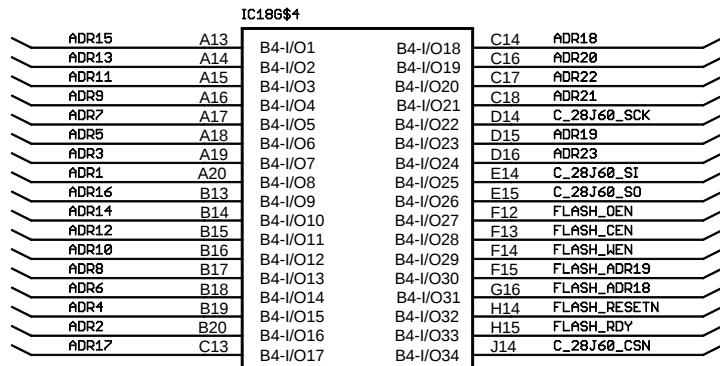
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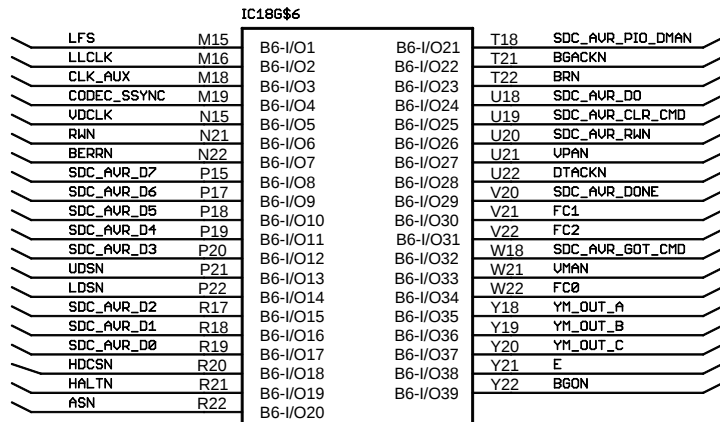
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2K7A-WF

Date: 12.10.2007 15:40:13

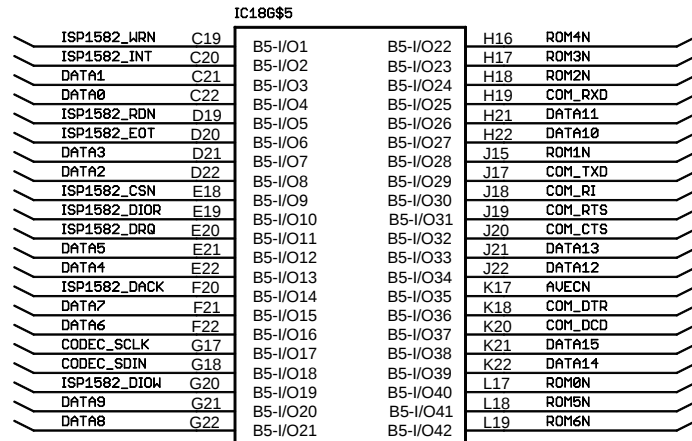
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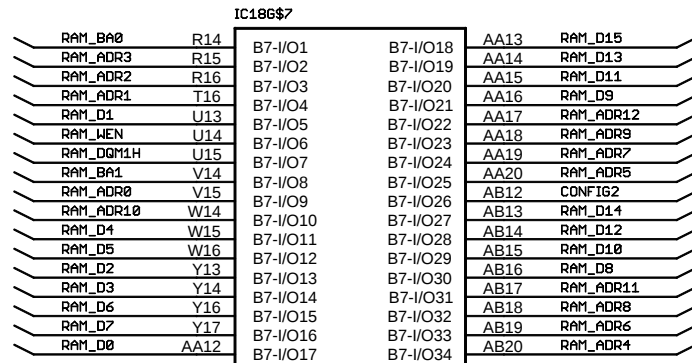
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EP2C35F484



EP2C35F484



EP2C35F484

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FPGA (part 2)

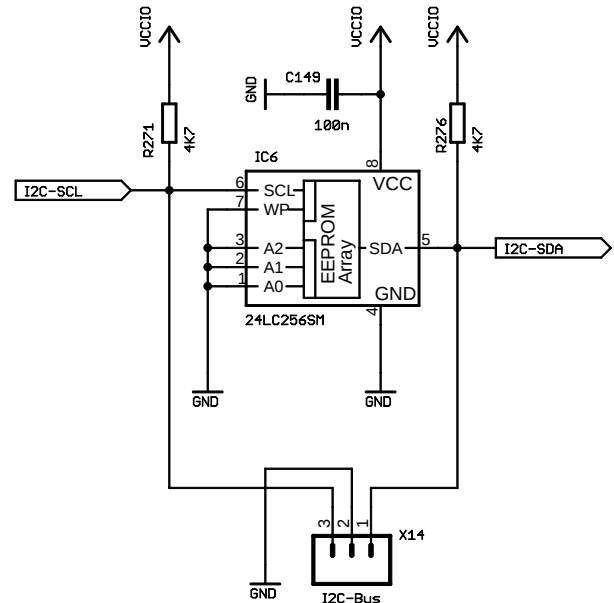
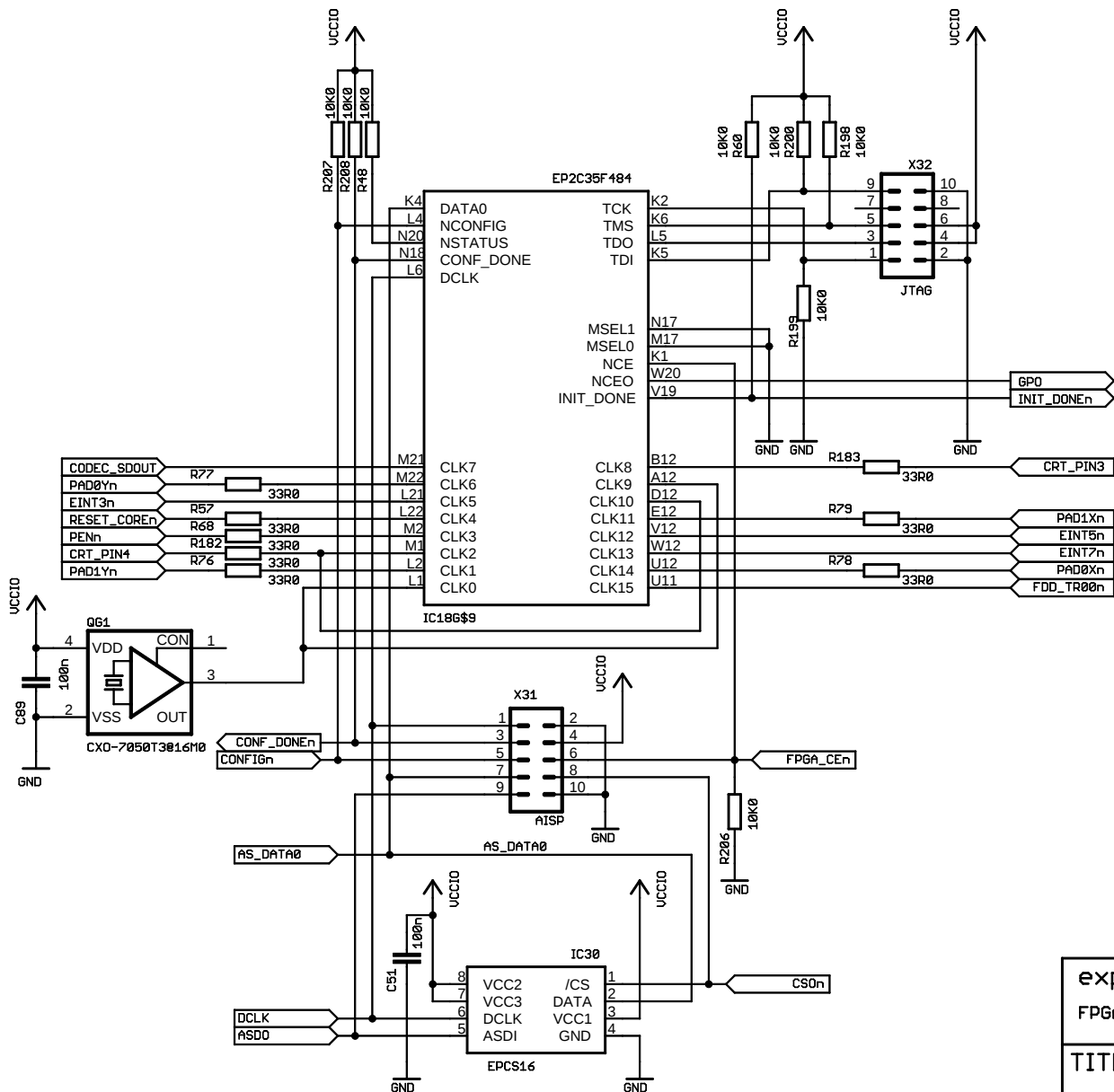
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REV:
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Date: 12.10.2007 15:40:13

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FPGA (part 3); Serial EEPROM

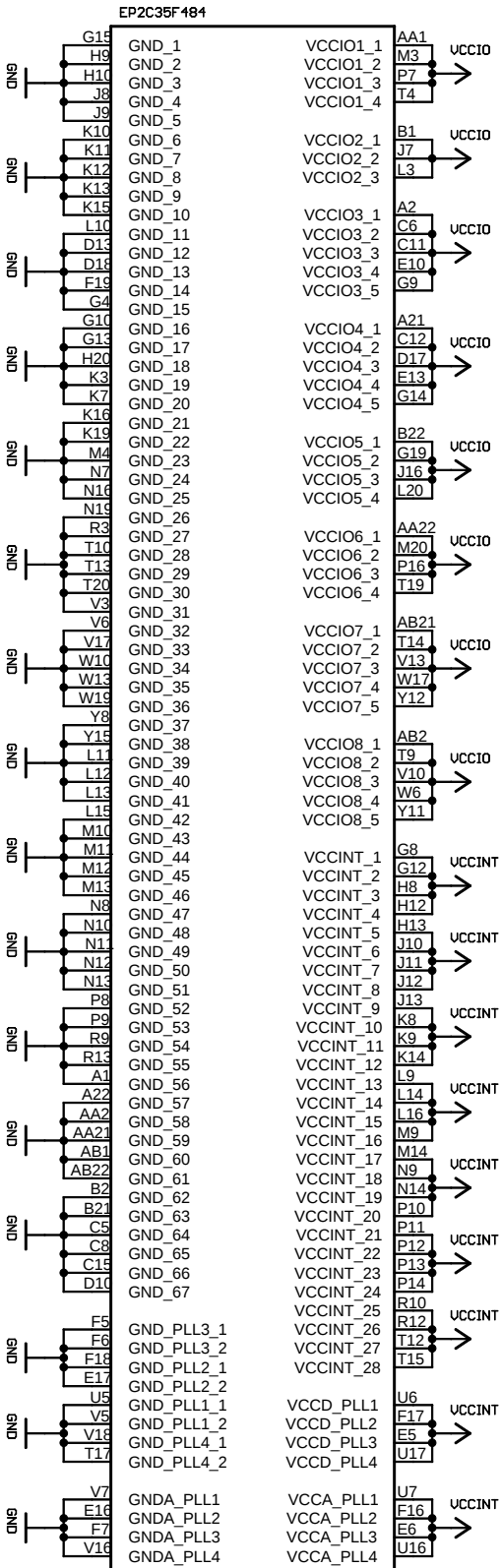
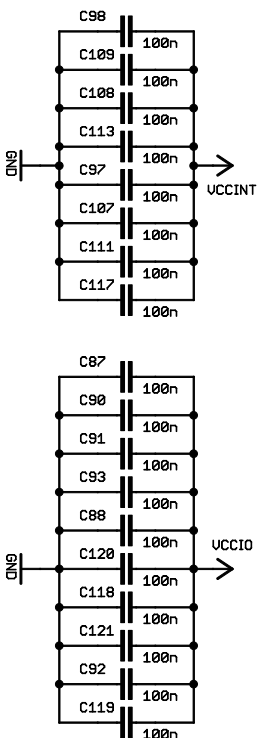
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2K7A-WF

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FPGA (part 4)

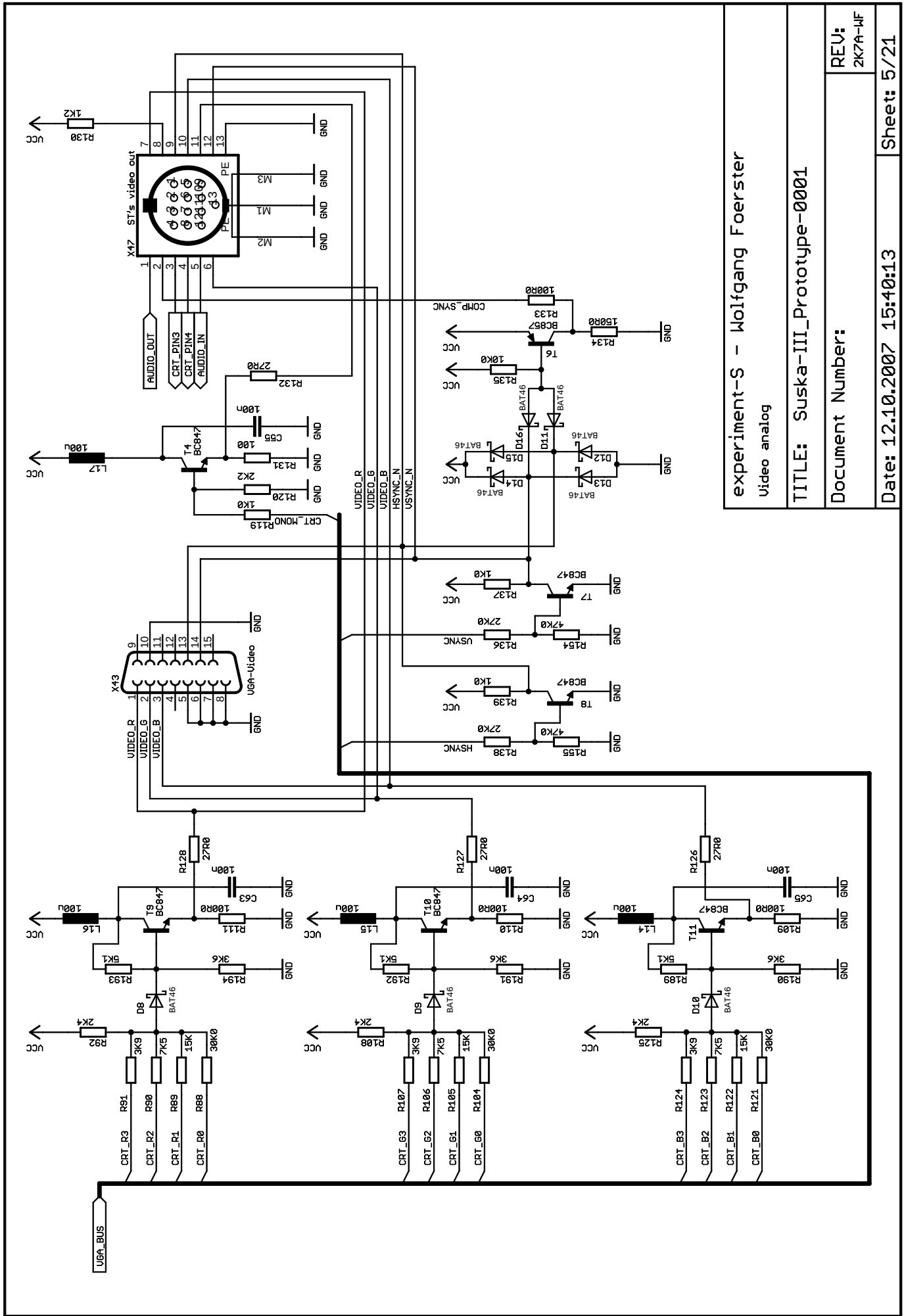
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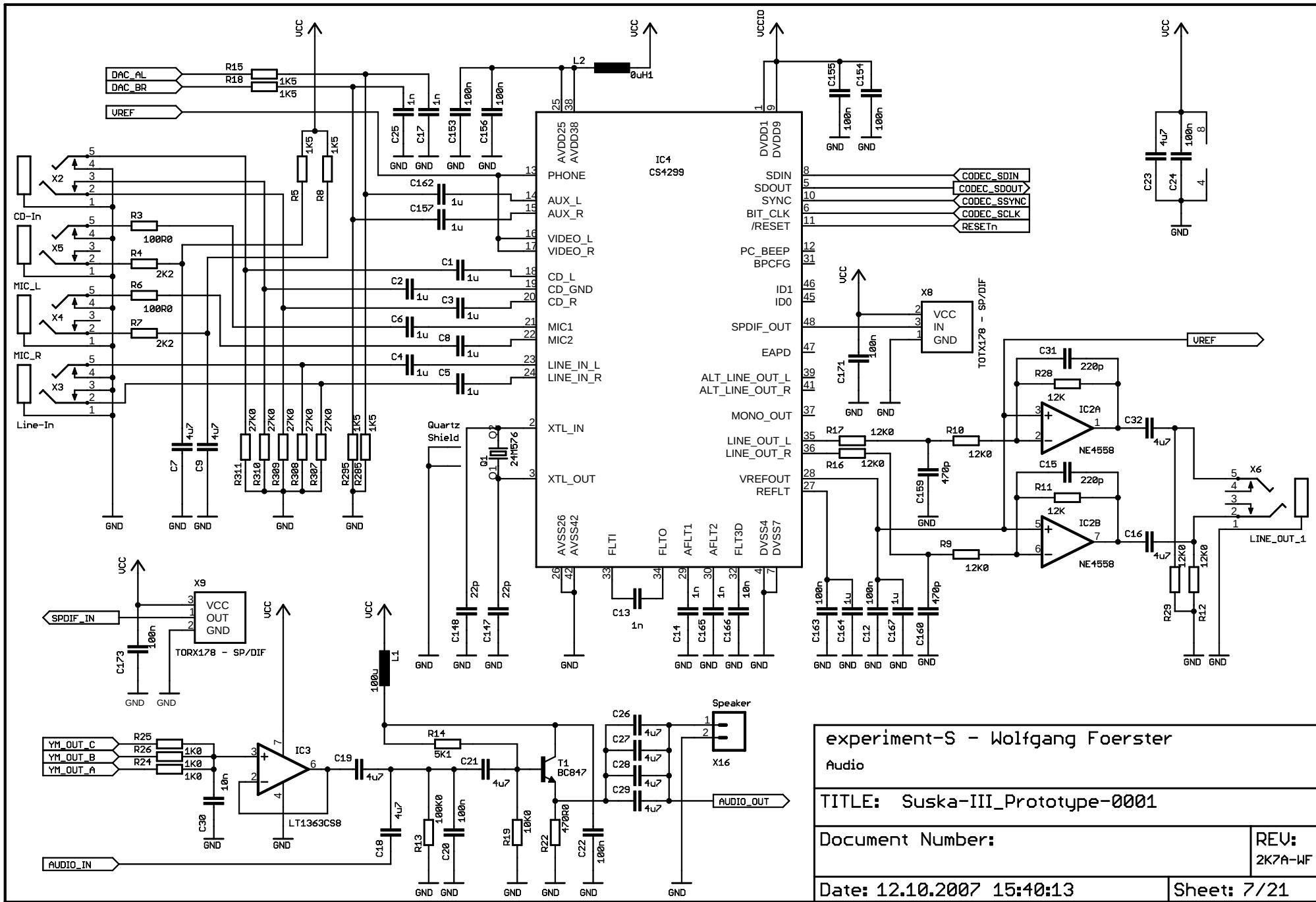
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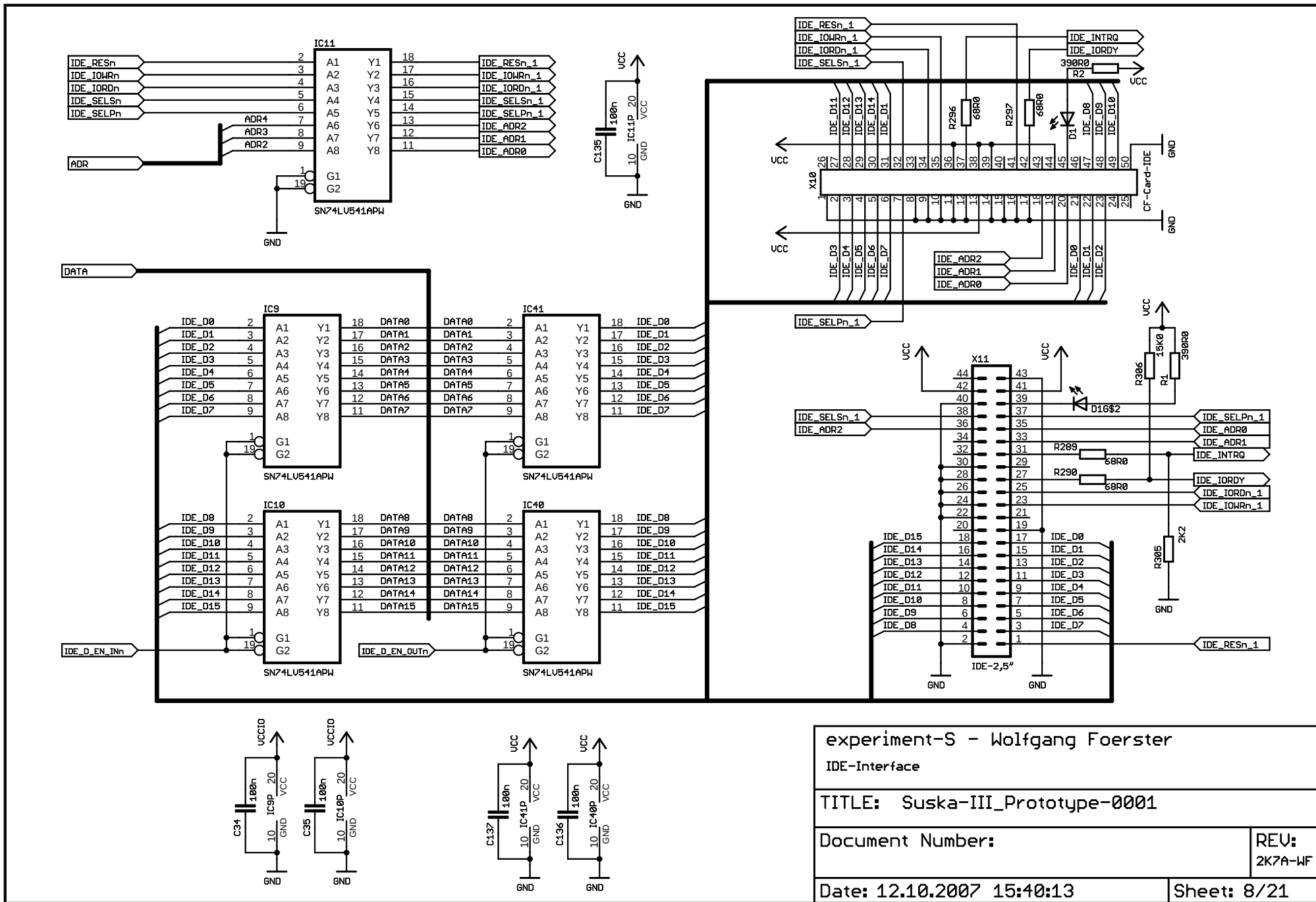
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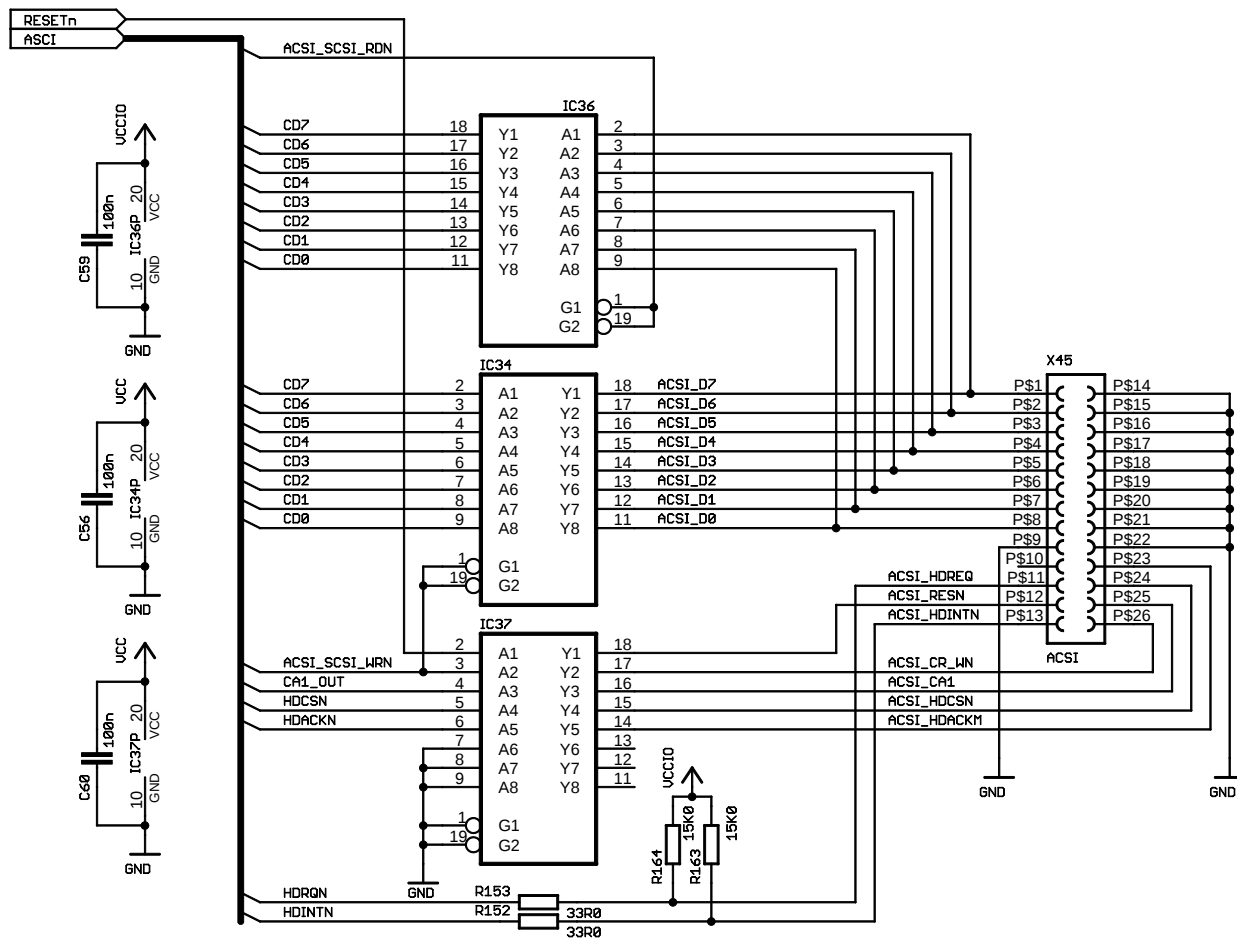
REV:
2K7A-WF







experiment-S - Wolfgang Foerster	
IDE-Interface	
TITLE: Suska-III_Prototype-0001	
Document Number:	REV: 2K7A-WF
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ACSI-Interface

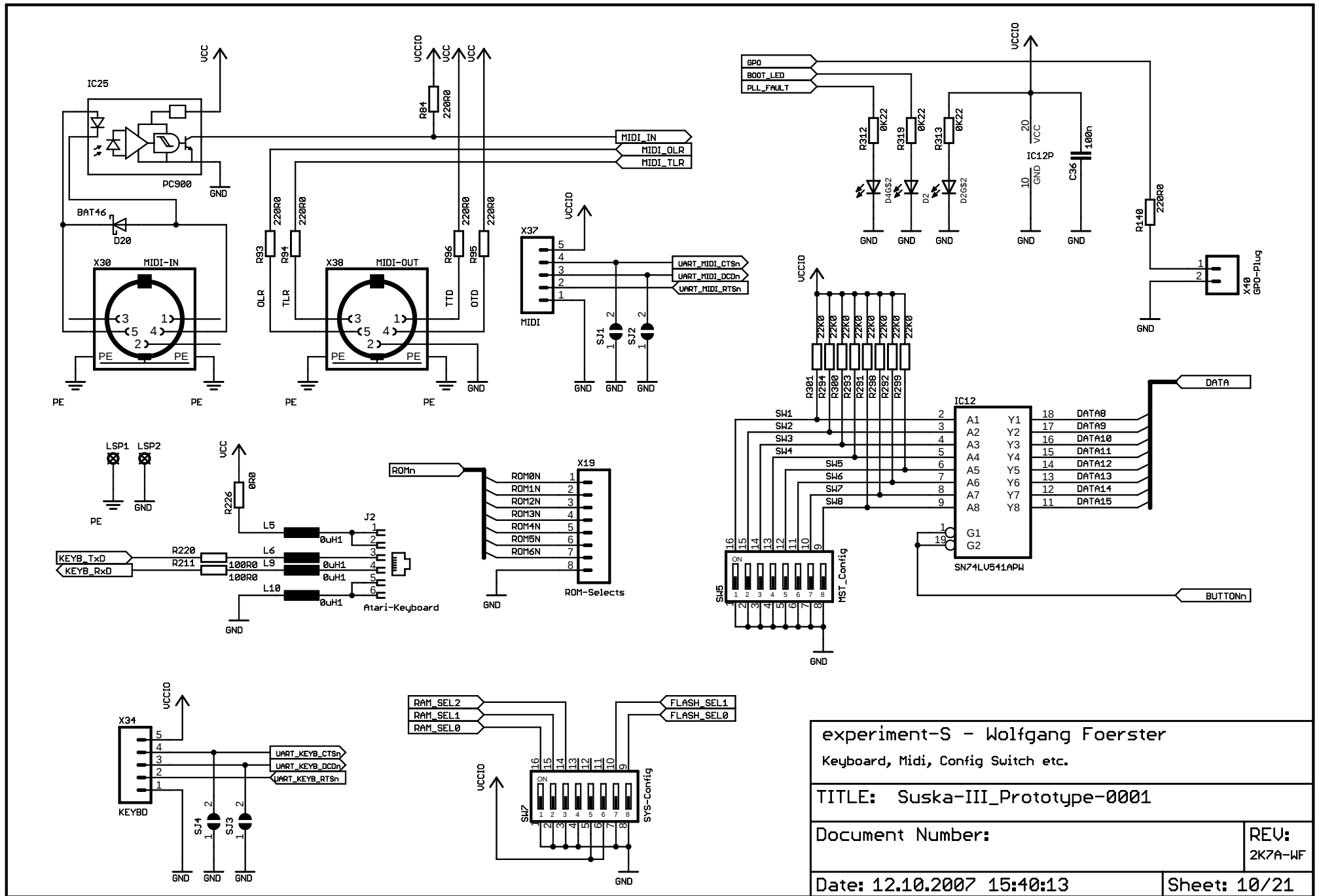
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Keyboard, Midi, Config Switch etc.

TITLE: Suska-III_Prototype-0001

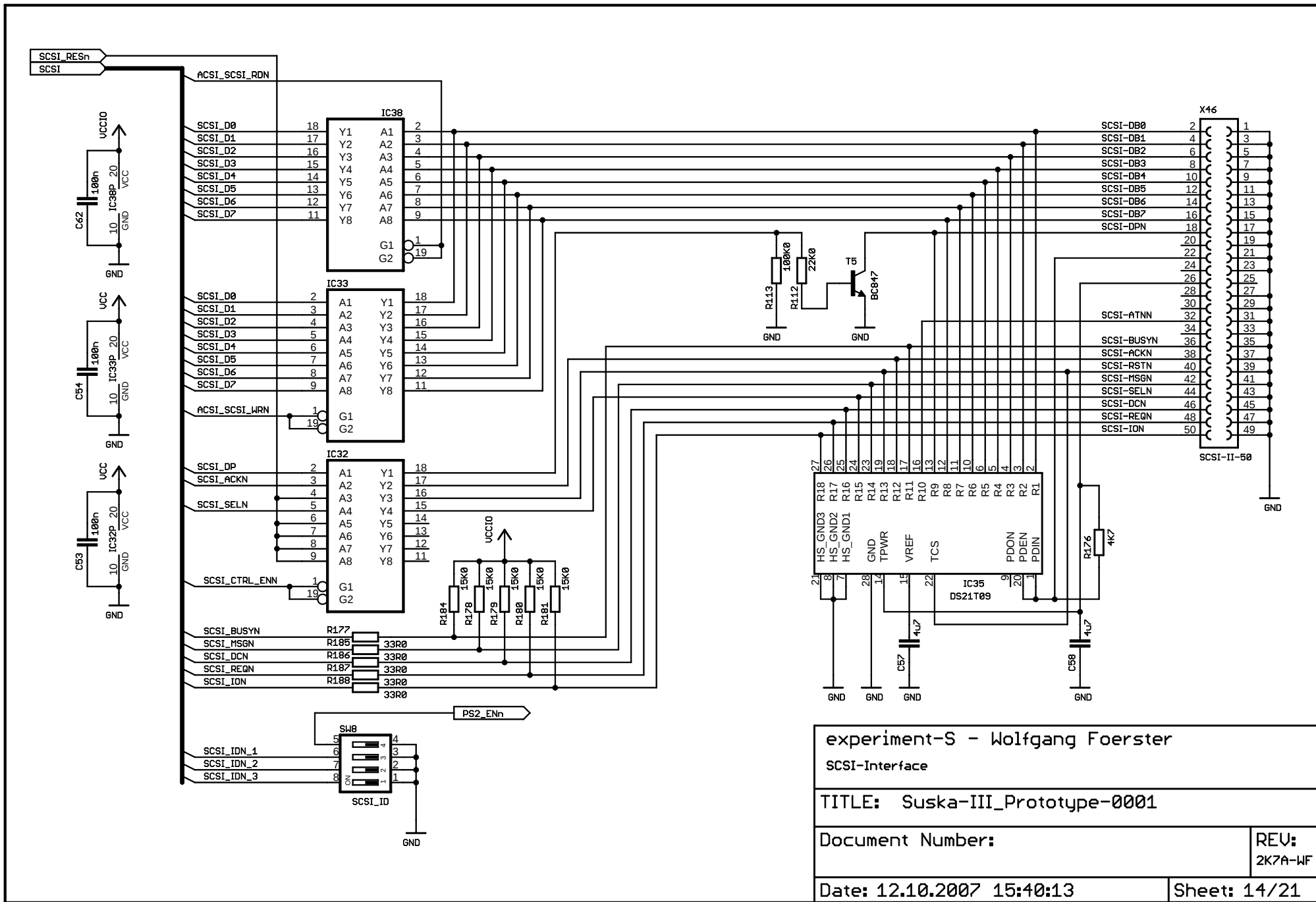
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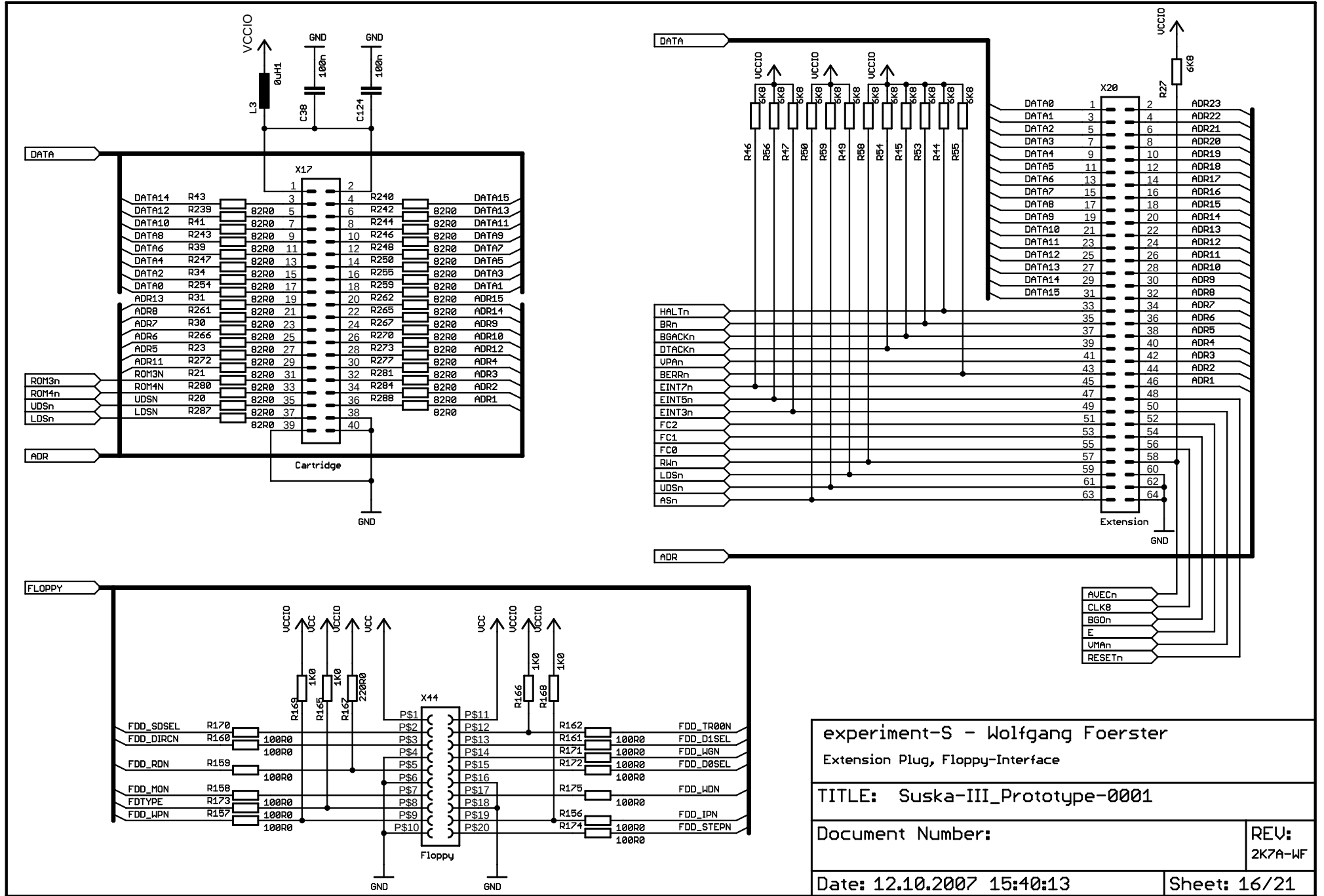
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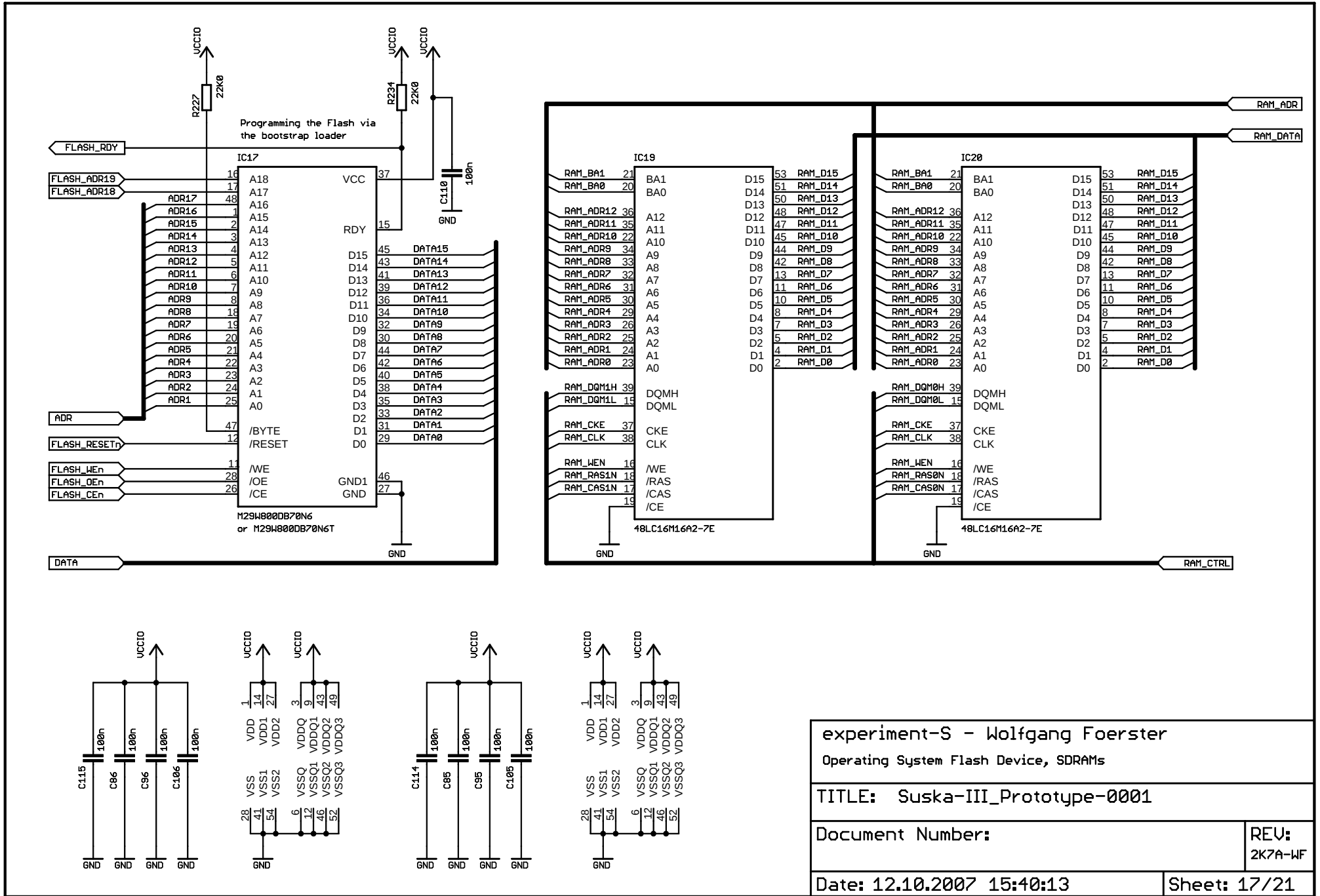
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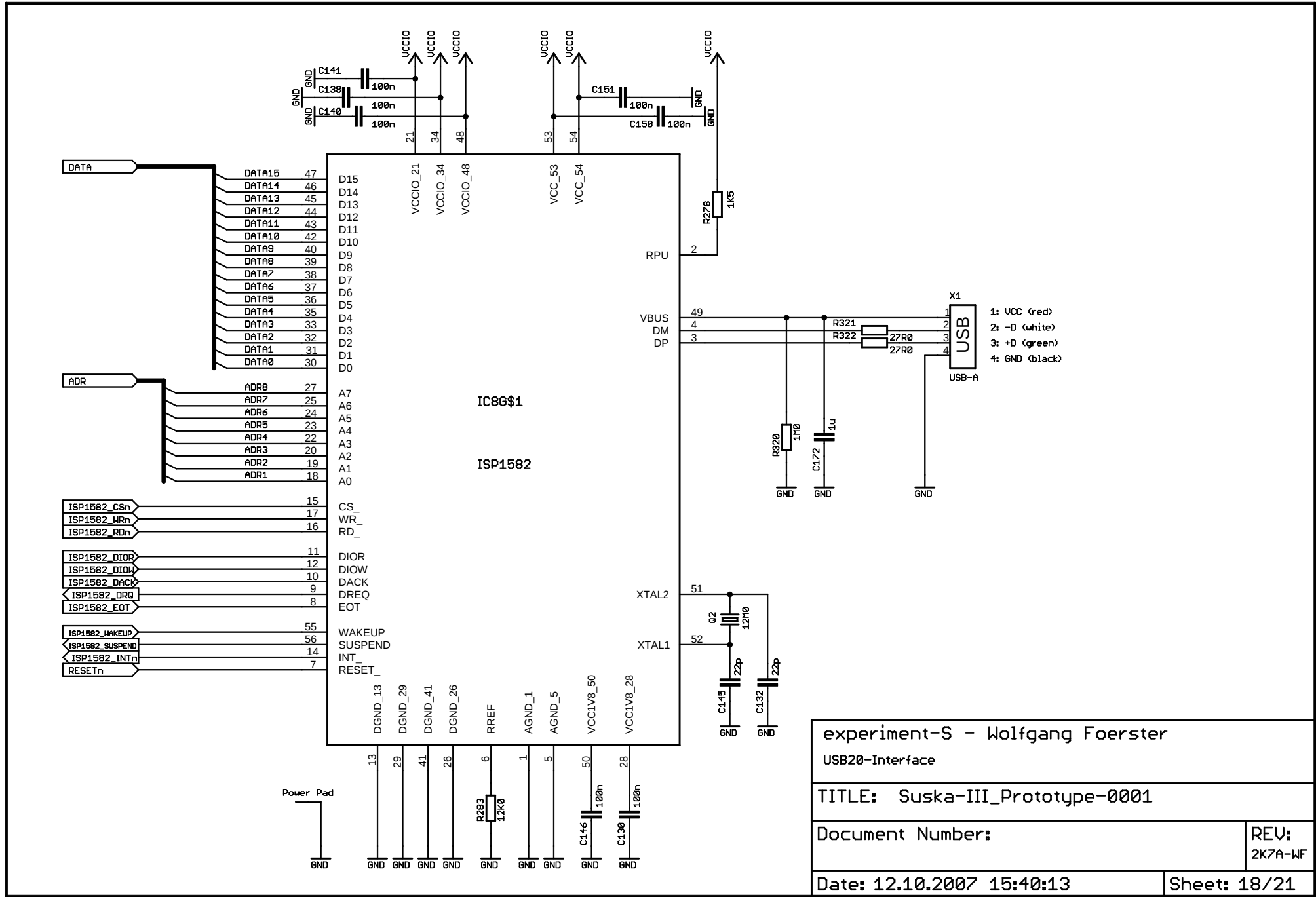








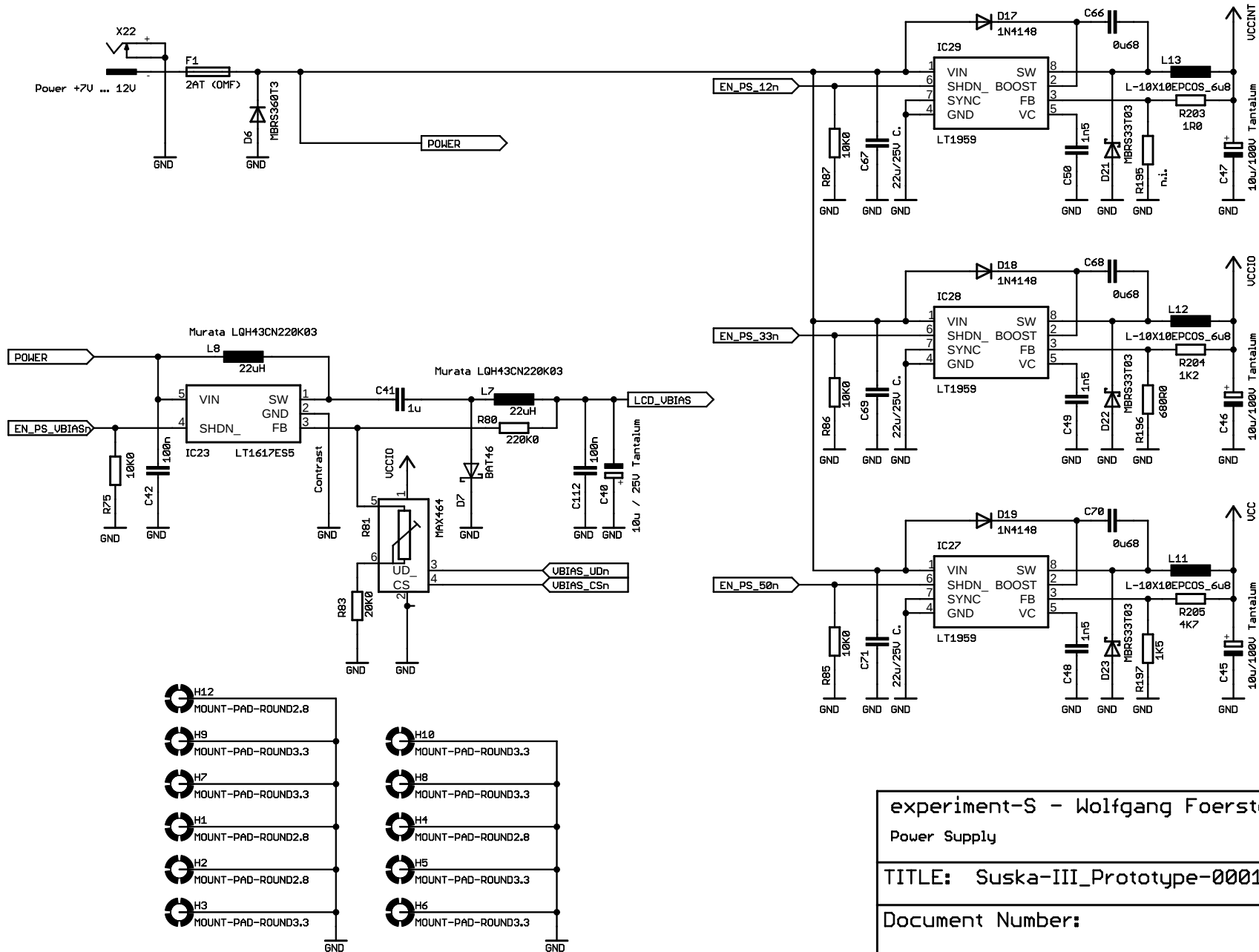
experiment-S - Wolfgang Foerster	
Operating System Flash Device, SDRAMs	
TITLE: Suska-III_Prototype-0001	
Document Number:	REV: 2K7A-WF
Date: 12.10.2007 15:40:13	Sheet: 17/21



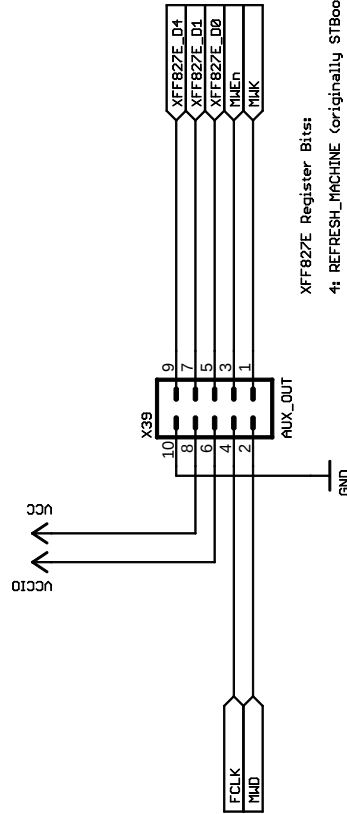
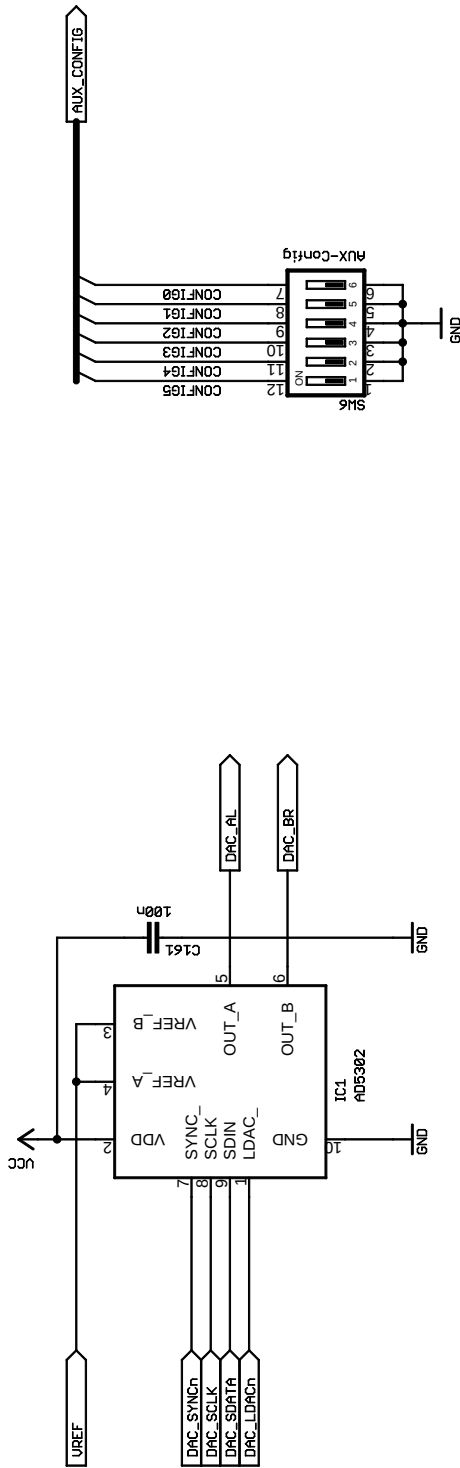
experiment-S - Wolfgang Foerster	
USB20-Interface	
TITLE: Suska-III_Prototype-0001	
Document Number:	REV: 2K7A-WF
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XFF827E Register Bits:

- 7: MTR_POWER_ON (Turns on IDE drive motor).
- 6: Not further specified.
- 5: RS232_OFF.
- 3: LAMP (LCD backlight, STBook).
- 2: POWER_OFF



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Power Supply	
TITLE: Suska-III_Prototype-0001	
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XFF827E Register Bits
 4: REFRESH_MACHINE (originally STBook).
 1: SHFT output (Shifter data stream control, STBook).
 0: SHAD0W chip off (STBook).

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 Audio-DA-Converter, Spares

TITLE: Suska-III_Prototype-0001

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