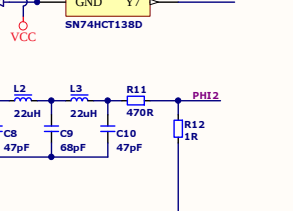
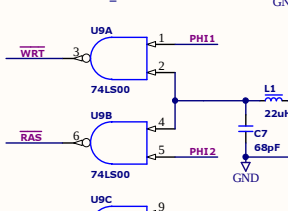
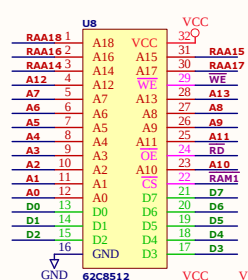
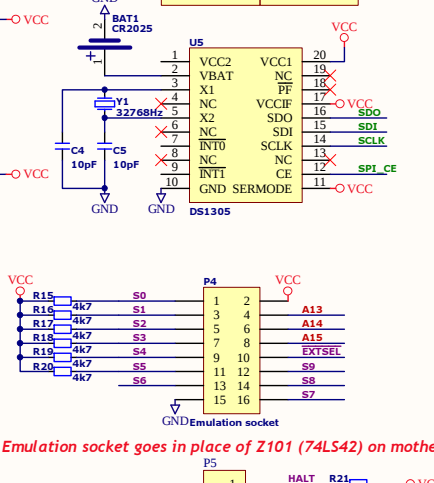
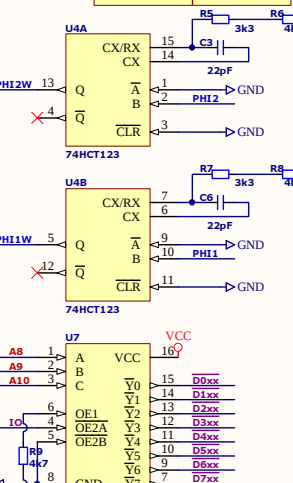
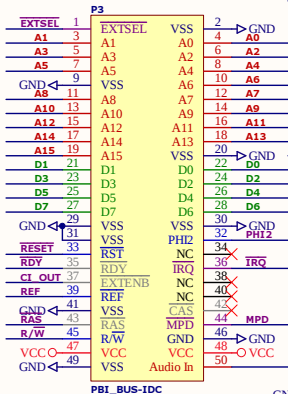
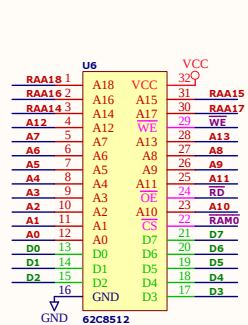
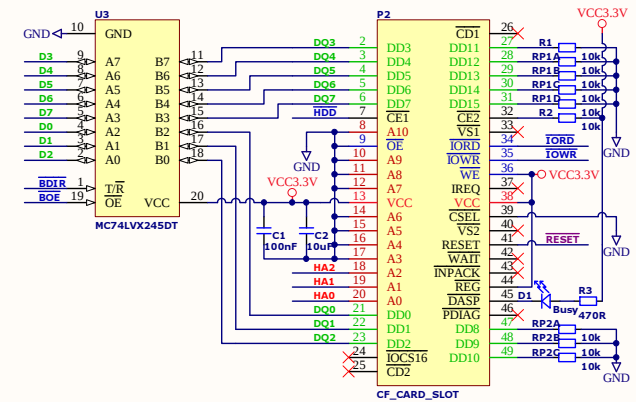
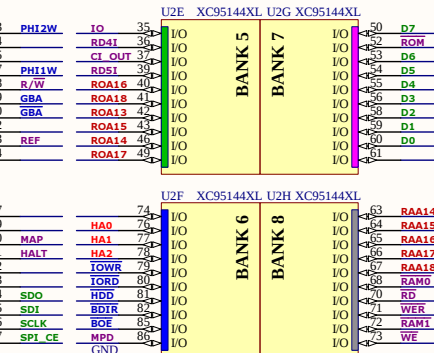
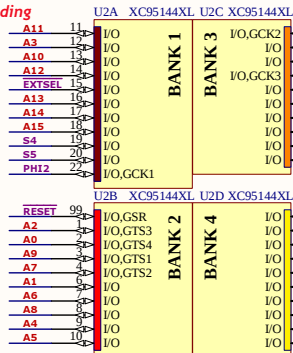
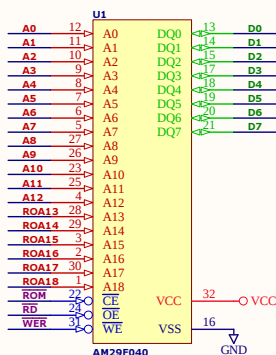
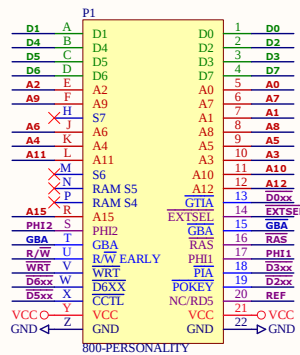
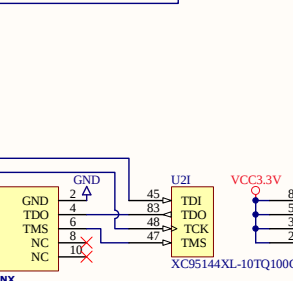
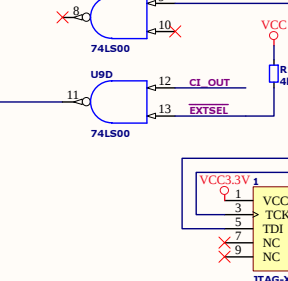
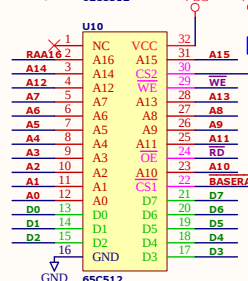


RAM slot #3 is fully functional
Signals S0-S3 (RAM slots #1 and #2) are disabled, making these slots disabled
Signals S6 and S7 are not used, since full address buss is present for address decoding

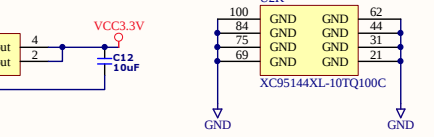
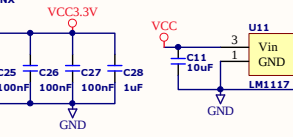
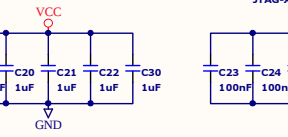
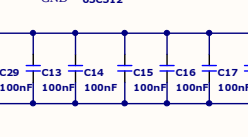
Signals nWRT and nRAS are present for compatibility with any RAM SLOT 3 extension, and are exact copy of their original counterparts



Emulation socket goes in place of Z101 (74LS42) on motherboard



Auxiliary connector for proper memory expansion and PBI emulation
BIOS code takes care of RNMI/RESET vector handling, so operating system will always start from valid address regardless of machine type selected (400/800 or XL/XE)



Title		
INCIGNITO BOARD		
Size	Number	Revision
A3	1.0	1.0
Date:	2012-01-29	Sheet1 of 1
File:	D:\Private\Incognito\Incognito.SchDoc	Drawn By: Sebastian Bartkowicz