



RIGID DISK DRIVE



PRODUCT SPECIFICATION AND USER'S MANUAL

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TM252 RIGID DISK DRIVE PRODUCT SPECIFICATION AND USER'S MANUAL





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SECTION 1

GENERAL DESCRIPTION

INTRODUCTION

This manual provides useful information to assist the customer when incorporating the Tandon rigid disk drive into a system.

Tandon Corporation's TM252 drive is a full feature, 12.75 megabyte, one-half height, 5-1/4-inch, rigid disk drive. It is a compact data storage device containing two 130-millimeter plated aluminum recording platters and four recording heads within a sealed housing.

1.1 SCOPE OF THE DOCUMENT

Section 1 of this manual contains a general description of the disk drive. Section 2 contains the product specifications. Section 3 provides information on operation of the drive.

1.2 PURPOSE OF THE DRIVE

The 5-1/4-inch disk drive is a rotating disk memory device designed for random access data storage and retrieval. Typical applications include word processing systems, entry level microprocessor systems, intelligent calculators, program storage, small business computer systems, and any application in which low cost, random access data storage is required.

1.3 MAJOR FEATURES

MICROPROCESSOR CONTROL

The TM252 drive features an onboard microprocessor, providing five major functions:

- 1. Self-calibration on power-up.
- 2. Buffered seek timing for improved access times.
- 3. Improved positioning with reduced hysteresis.
- 4. Write current switching for optimal recording quality.
- 5. Power and track fault detection.

DAISY CHAIN CAPABILITY

The drive provides the address selection and gating functions necessary to daisy chain a maximum of four units at the user's option. The last drive on the daisy chain terminates the interface. The terminations are accomplished by a resistor array plugged into a DIP socket.

INDUSTRY STANDARD INTERFACE COMPATIBILITY

The drive is compatible with controllers that use an ST506 industry standard interface.

AIR FILTRATION

A self-contained, recirculating air filtration system supplies clean air through a 0.3-micron filter. A secondary absolute filter is provided to allow pressure equalization with the ambient atmosphere without contamination. The entire head-disk-actuator compartment is maintained at a slightly positive pressure to further ensure an ultraclean environment.

COMPACT SIZE

The reduced height of the drive occupies only onehalf the mounting space required for a conventional drive.

1.4 FUNCTIONAL DESCRIPTION

The drive is fully self-contained, and requires no operator intervention during normal operation. During the power-up sequence, the spindle motor reaches 3,600 RPM, and the positioning mechanism recalibrates the recording heads to Track 0. At this time, a Ready signal on the interface indicates the drive is ready for operation.

The head is positioned over the desired track by means of a four-phase stepper motor/band assembly and its associated electronics. This positioner uses a one-step rotation to cause a one-track radial movement. Subsequently, the recording heads can be positioned over the desired cylinders, and the data can be read or written from the appropriate track by selecting the desired head.

Typically, the drive uses MFM write and read data recording methods. Data recovery electronics include a low-level read amplifier, differentiator, a zero-crossover detector, and digitizing circuits. No data encoding or decoding feature is provided on the drive.

The drive has the following sensor systems:

1. An optical Track 0 switch senses when the Head/Carriage Assembly is positioned at Track 0.

2. An index sensor, which consists of a magnetic pick-up and index hole positioned to provide an analog signal when an index hole is detected.

1.5 PHYSICAL DESCRIPTION

A representative drive is shown in Figure 1-1. The drive contains 130 millimeter storage media that rotate at 3,600 RPM, using a direct drive, brushless D. C. motor. The recording is accomplished by non-contact standard recording heads that are moved by an open loop, precision split band positioning device and stepper motor.



FIGURE 1-1 DISK DRIVE

The Head Disk Assembly is enclosed in a sealed cast aluminum housing, which includes an air filtration system to ensure a contamination-free environment. The housing is shock mounted to a metal frame that has threaded holes on the sides and bottom for mounting the drive onto a chassis. An optional snap-on front cover may be attached.

In addition, the drive includes the read/write and control electronics, servo spindle control electronics, and an index sensor.

SECTION 2

PRODUCT SPECIFICATIONS

INTRODUCTION

This section contains the mechanical and operational, reliability and environmental specifications for the TM252 disk drives.

2.1 MECHANICAL SPECIFICATIONS

The mechanical and physical dimensions are contained in Figure 2-1.

2.2 ELECTRICAL AND OPERATIONAL SPECIFICATIONS

The electrical and operational specifications are contained in Table 2-1. Typical starting current requirements at nominal voltage are contained in Figure 2-2.

2.3 RELIABILITY SPECIFICATIONS

The reliability specifications are contained in Table 2-2.

2.4 ENVIRONMENTAL SPECIFICATIONS

The environmental specifications are contained in Table 2-3.

This product is recognized under U. L. EMRT2, Component-Data Processing Equipment, Electronic.



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TABLE 2-1 ELECTRICAL AND OPERATIONAL SPECIFICATIONS

Media	Hardcoated, 130 millimeter, plated aluminum disk			
Number of Disks	2	2		
Tracks Per Inch	345 TPI	345 TPI		
Spacing, track to track	2.9 milinches	2.9 milinches		
Number Of Cylinders	306 cylinders	306 cylinders		
Number Of Tracks	1,224 tracks			
Disk Speed	3,600 RPM \pm 1 percent			
Average Latency	8.33 milliseconds			
Start Time	15 seconds maximum			
Stop Time	15 seconds maximum			
Seek Time, track to track	3 milliseconds	3 milliseconds		
Head Settling Time, last track accessed	15 milliseconds	15 milliseconds		
Average Access Time, including head settling time, 3 millisecond step rate	321 milliseconds			
Average Access Time Using Buffered Seek, including head settling time	85 milliseconds			
Transfer Rate	5 megabits per second			
Tandon corporation, CHATS	SWORTH, CALIFORNIA 91311	179050-001 REV. B		

TABLE 2-1 (CONTINUED) ELECTRICAL AND OPERATIONAL SPECIFICATIONS

7	and	On CORPORATION, CHATSWORTH	, CALIFORNIA 91311	179050-001 REV. B
		There are no restrictions in sequenci	ng supplies on or off.	
		$+5$ volts D. C. ± 5 percent, 0.5 amperators more than 50 millivolts PARD.	eres typical, 0.8 amperes maxir	num running, with no
	nunning:	+ 12 volts D. C. \pm 10 percent, 1.4 and than 50 millivolts Periodic and Rance	lom Deviation (PARD).	
	Start Up: Running:	+ 12 volts D. C., 2.0 amperes typica		
		QUIREMENTS		
	Unformatt	ed Capacity Per Track	10.4 kilobytes	
		ed Capacity Per Surface	3.19 megabytes	
		ed Capacity Per Drive	12.76 megabytes	
	Maximum	Flux Reversal Density	9,090 FRPI	



TABLE 2-2 **RELIABILITY SPECIFICATIONS**

SOFT AND HARD READ ERROR RATES, EXCLUSIVE OF MEDIA DEFECTS

For data that has been verified previously as error free, and when used in conjunction with a data separator and phase lock loop of good design, the recoverable (soft) read error rate for any subsequent read operation shall not exceed one error in 1 X 10^{10} bits transferred. A recoverable read error is an error that may be corrected within five attempts to reread the data.

The nonrecoverable (hard) read error rates shall not exceed one error in 1 X 10¹² bits transferred. A nonrecoverable read error is an error that may not be corrected within five attempts to reread data, providing that the writing of the data previously has been verified as correct. The seek error rate is not to exceed one error in 1 X 10^6 seeks.

MEDIA DEFECTS

Any defects on the media surface will be identified on a defect map provided with each drive. This defect map will indicate the head number, track number, and number of bytes from index for each defect. Each defect shall be no longer than 16 bits. More than one defect per track is to be counted as one defect. Cylinders 000 and 001 are guaranteed error free.

The defect map is offered as a guide only. The number of defects and their location can change due to customer system variations such as data separators.

NOTE

Prior to shipment from the factory, the heads are parked at Cylinder 305. This must be done each time the drive is to be relocated, and should be done prior to a power shutdown sequence.

Maximum Allowable Defects	4 per surface, 10 per drive
Mean Time Between Failures	11,000 power on hours (typical useage)
Mean Time To Repair	30 minutes
Component Design Life	5 years
Preventative Maintenance	Not required

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TABLE 2-3 ENVIRONMENTAL SPECIFICATIONS

Ambient Temperature		
Ambient Temperature		
Operating	4° C to 50°C, 39°F to 12	
Nonoperating	-40° C to 60° C, -40° I	F to 140° F
Temperature Gradient		
Operating	10°C per hour, 18°F pe	r hour
Nonoperating	Below that causing cond	ensation
Relative Humidity	8-to-80 percent, noncond	lensing
Relative Humidity Gradient		
Operating	20 percent per hour	
Nonoperating	Below that causing cond	ensation
Maximum Wet Bulb Temperature 26°C, 78.8°F, without condensation		ondensation
Elevation		
Operating	Density Altitude: - 457 to 3,050 meters, - 1,500 to 10,000 feet	
Nonoperating -457 to 12,195 r -1,500 to 40,000		
Heat Dissipation	78 BTU per hour, 22.5 v	vatts
	TH, CALIFORNIA 91311	179050-001 REV. B

TABLE 2-3 (CONTINUED) ENVIRONMENTAL SPECIFICATIONS

Vibration	
Operating	The drive will operate with a read/write error rate no greater than the specified limit while sub- jected to vibration levels at 5 to 500 Hz of 0.5 G, maximum, input. Dwell at resonant frequencies for 30 minutes. Resonant frequencies are de- fined as frequencies where 1.0 G or higher in/out response is created.
Nonoperating	A constant acceleration input may be applied with the desired frequency swept, stopping at resonant frequencies for 30 minutes. The drive will meet specified error rates and have no addi- tional media defects after vibraiton levels at 5 to 500 Hz of 2 G's, maximum, are applied in each of the 3 mutually perpendicular axes, 1 axis at a time.
Shock	
Operating	The drive will operate with an error rate no greater than the specified limit while a half sine wave shock pulse of 10 G's with a duration of 11 milliseconds is applied in either direction, in each of the 3 perpendicular axes, 1 axis at a time.
Nonoperating	The drive will survive a series of 6 drops, 1 each side, top and bottom, a minimum of 40 G's squarewave shock pulse of 10 milliseconds duration.
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SECTION 3

OPERATION

INTRODUCTION

This section contains information pertinent to the handling, inspection, installation, and operation of the TM252 drive.

3.1 UNPACKING THE DRIVE

Each drive is shipped in a protective container which, when bulk packaged, minimizes the possibility of damage during shipment.

The following procedure is recommended for unpacking the drive.

- 1. Place the shipping container on a flat work surface.
- 2. Visually examine the shipping container for damage.
- 3. Cut the tape on the shipping container.
- 4. Remove the foam lid and pads from the shipping container.
- 5. Remove the inner container.
- 6. Remove the drive from the inner container.
- 7. Place the drive on a foam lined surface.
- 8. Notify the carrier immediately if any damage is found.

CAUTION

Do not manually rotate the stepper motor or spindle motor. Damage to the heads and disk may result.

NOTE

The inside chamber of the drive is a sealed compartment that must not be opened.

When returning the drive to the service center, park the heads at cylinder 305 (see page 2-6). Be sure to use the prior steps in reverse order, and ensure the foam stiffeners are in the proper location, with the cardboard dividers properly in place between the drives (see Figure 3-1).

3.2 PREINSTALLATION CHECKOUT

Before applying power to the drive, inspect for the following:

- 1. Ensure the front panel is secure.
- 2. Ensure the circuit board is secure.
- 3. Ensure the connectors are firmly seated.
- 4. Ensure there is no debris or foreign material between the frame and the head/disk casting.
- 5. Ensure the head/disk housing can move freely on the shock mounts of the frame.
- 6. Ensure the termination resistor pack and jumper blocks are firmly seated and in the correct configuration.

3.3 MOUNTING THE DRIVE

The drive can be mounted in any vertical or horizontal plane. Eight 6-32 tapped holes are provided for mounting: four on each side and four on the bottom of the frame (see Figure 2-1). The drive is manufactured with some critical internal alignments that must be maintained. Hence, it is important the mounting hardware does not introduce significant stress on the drive.

Any mounting scheme in which the drive is part of the structural integrity of the enclosure is not permitted. Mounting schemes should allow for adjustable brackets or incorporate resilient members to accommodate tolerances.

DUST COVER

The design of an enclosure should incorporate a means to prevent contamination from loose items, e. g., dust, lint, and paper chad since the drive does not have a dust cover.

FREE AIR FLOW

When the drive is mounted so the components have access to the free flow of air, normal convection cooling allows operation over the specified temperature range (see Table 2-3).

CONFINED ENVIRONMENT

When the drive is mounted in a confined environment, air flow must be provided to maintain specified air temperatures in the vicinity of the motors and the circuit boards.

3.4 INTERFACE CONNECTORS

The electrical interface between the drive and the host system is via three connectors. J1 provides control signals for the drive (see Figure 3-2). J2 provides for the radial connection of read/write data signals (see Figure 3-3). J3 provides for D. C. power (see Figure 3-4).

Table 3-1 contains interface lines. The interface description of the connectors, and the location of each, is contained in this section.

J1/P1 CONNECTOR

Connection to J1 is through a thirty-four-pin circuit board connector. Figure 3-2 contains the dimensions of this connector. The pins are numbered 1 through 34. The recommended mating connector for P1 is 3M ribbon connector P/N 3463-0001, without ears. A key slot is provided between Pins 4 and 6.

J2/P2 CONNECTOR

Connection to J2 is through a 20-pin circuit board connector. Figure 3-3 contains the dimensions of this connector. The recommended mating connector for P2 is 3M ribbon connector P/N 3461-0001, without ears. A key slot is provided between Pins 4 and 6.

J3/P3 CONNECTOR

D. C. power connector J3 is a four-pin AMP Mate-N-Lok connector, P/N 350211-1, mounted on the component side of the circuit board. The recommended mating connector, P3, is AMP P/N 1-480424-0, utilizing AMP pins P/N 60619-4. J3 pins are labeled on the J3 connector (see Figure 3-4). J3 cabling must be 18 AWG, minimum.

GROUND CONNECTOR

A ground connector, located on the rear of the head/disk assembly is Faston AMP P/N 61761-2. The recommended mating connector is AMP P/N 62187-1. To realize error rates (see Table 2-2), it must be connected directly to the centrally located system logic ground via an 18 AWG, minimum, cable.



FIGURE 3-1 SHIPPING CONTAINER, SINGLE PACK



FIGURE 3-1 (CONTINUED) SHIPPING CONTAINER, MULTIPACK



FIGURE 3-2 J1 EDGE CONNECTOR DIMENSIONS



FIGURE 3-3 J2 EDGE CONNECTOR DIMENSIONS



FIGURE 3-4 J3 POWER CONNECTOR

		rface umber			
Connector	Signal	Ground	Signal Type	I/0	Name of Signal
P1	2	(1)	S	Ι	Spare
†	4	(3)	S	Ι	Spare
	6	(5)	S	Ι	Write Gate
	8	(7)	S	0	Seek Complete
	10	(9)	S	0	Track 0
34-Pin	12	(11)	S	0	Fault
Ribbon	14	(13)	S	Ι	Head Select 2 [°]
Daisy	16	(15)			Reserved (To J2-7)
Chain	18	(17)	S	Ι	Head Select 2 ¹
	20	(19)	S	0	Index
	22	(21)	S	0	Ready
	24	(23)	S	Ι	Step
	26	(25)	Š	Ι	Drive Select 0
	28	(27)	ŝ	Ī	Drive Select 1
	30	(29)	Š	Ī	Drive Select 2
	32	(31)	Š	Î	Drive Select 3
P1	34	(33)	ŝ	Ī	Direction In
		()			
P2	1	(2)	S	0	Drive Select
↑	3	(4)	S,	_	Spare
	5	(6)		_	Reserved
	7	(8)			Reserved (To J1-16)
20-Pin	9	(10)		_	Spare
Ribbon	11	(12)	_	_	Ground
Radial	13		D	I	+ Write Data
	14		D	Ι	– Write Data
	15	(16)			Ground
	17		D	0	+ Read Data
¥	18		D	Ο	– Read Data
P2	19	(20)			Ground
P3	1				+ 12 volts D. C. In
4-Pin	2				+ 12 volts D. C. Return
Radial	3				+ 5 volts D. C. Return

TABLE 3-1 DRIVE INTERFACE SIGNALS AND PIN ASSIGNMENTS

NOTES: S

Single Ended Differential =

D I =

=

Drive Input Drive Output 0 =

3.5 INTERFACE LINE DESCRIPTIONS

The interface for the TM252 drive is available in one configuration. It is compatible with industry standard drives. Compatibility is defined as using the same pin assignment where the signal and function are common. Table 3-1 contains pin assignments.

The interface may be connected in the radial or daisy chain configuration (see Figures 3-5 and 3-6).

INPUT CONTROL SIGNALS

The input control signals are of two kinds: those to be multiplexed in a multiple drive system and those that do the multiplexing. The input control signals to be multiplexed are: Reduced Write Current, Write Gate, Head Select Line 2^0 , Head Select Line 2^1 , Step, and Direction In. The multiplexing signal is Drive Select 0, Drive Select 1, Drive Select 2 or Drive Select 3.

The input signals have the following electrical specifications, as measured at the drive. Figure 3-7 illustrates the recommended circuit.

True: 0.0 volt D. C. to 0.4 volt D. C. at I = -40 milliamperes, maximum

False: 2.5 volts D. C. to 5.25 volts D. C. at I = 250 microamperes, maximum (open)

All input signals share a 220/330 ohm resistor pack for line termination. Only the last drive in the chain should have the resistor pack installed.



FIGURE 3-5 RADIAL CONFIGURATION



FIGURE 3-6 DAISY CHAIN CONTROL LINES



FIGURE 3-7 CONTROL SIGNAL/DRIVER RECEIVER CIRCUIT COMBINATION

WRITE GATE

The active state of this signal or logical zero level enables write data to be written on the disk. The inactive state of this signal enables the data to be transferred from the drive. In addition, the inactive state enables the step pulse to step the read/write actuator.

HEAD SELECT LINES 2⁰ AND 2¹

These two lines provide for the selection of each read/write head in a binary coded sequence. Head Select Line 2^0 is the least significant line. The heads are numbered 0 through 3. When all Head Select lines are false, Head 0 is selected. Table 3-2 describes which head is selected for the head select lines.

Head recovery time (head-to-head select, write-toread recovery, or read-to-write recovery) is 2.4 microseconds, maximum.

STEP

This interface line is a control signal that causes the read/write heads to move with the direction of motion defined by the Direction In line. The access motion is initiated at the logical trueto-logical false transition or the trailing edge of this signal pulse. Any change in the Direction In line must be made at least 100 nanoseconds before the true-to-false edge of the step pulse. The quiescent state of this line should be held logically false.

The read/write head moves at the rate of the incoming step pulses. The minimum time between successive steps is three milliseconds, except during execution of a buffered seek. The minimum pulse width is one microsecond. Figure 3-8 illustrates the step timing.

	Table 3-2 HEAD SELECT LINES				
Head Sel 2 ¹	ect Lines 2º	Head Selected			
1 1 0 0	1 0 1 0	0 1 2 3			
Ŭ	Ū	U			



FIGURE 3-8 STEP MODE TIMING

BUFFERED SEEK

The buffered seek uses an onboard microprocessor that calculates the most efficient seek algorithm for the user. The user need only issue step pulses in accordance with the timing shown (see Figure 3-9). Step pulses are issued in a 1:1 ratio to the cylinders moved. If more pulses are issued than there are cylinders left to move, the heads soft stop at the last cylinder.

The pulse repetition rate must not be less than five microseconds. The duty cycle may be varied, provided the one microsecond minimums are met (see Figure 3-9).



FIGURE 3-9 BUFFERED SEEK STEP PULSES

DIRECTION IN

This signal defines the direction of motion of the read/write head when the Step line is pulsed. An open circuit or logical false defines the direction as "out". If a pulse is applied to the Step line, the read/write heads move away from the center of the disk. If this line is true, the direction is defined as "in", and the read/write heads move in toward the center of the disk.

Seek Complete must be true prior to changing directions and the application of additional step pulses.

REDUCED WRITE CURRENT

The Reduced Write Current input line is terminated, but is not used in the TM252 drive. The microprocessor automatically switches write current.

DRIVE SELECT 0 THROUGH DRIVE SELECT 3

These control signals enable the selected drive's input receivers and output drivers. When logically false, the output drivers are open circuits and the input receivers do not acknowledge signals presented to them.

Selecting the appropriate jumper block at W9 through W12 determines which select line activates the drive.

NOTE

Only one drive may be selected at a time.

OUTPUT CONTROL SIGNALS

The output control signals are driven with an open collector output stage capable of sinking a maximum of 40 milliamperes in a true state, with a maximum voltage of 0.4 volt measured at the driver. When the line driver is in the false state, the driver transistor is off, and the collector cutoff is a maximum of 250 microamperes.

All J1 output lines are enabled by the respective Drive Select lines.

SEEK COMPLETE

The Seek Complete signal goes true when the read/write heads have settled on the final track at the end of a seek. Reading or writing should not be attempted when Seek Complete is false.

Seek Complete goes false:

- 1. When a recalibration sequence is initiated by the microprocessor at power on because the read/write heads are not over Track 0.
- 2. 500 nanoseconds, maximum, after the trailing edge of a step pulse or a series of step pulses.
- 3. When power is momentarily lost, Seek Complete is false when power is restored and remains false until an automatic recalibration is completed.

TRACK 0

The Track 0 signal indicates a true state only when the drive's read/write heads are positioned at Track 0, the outermost data track.

FAULT

The Fault signal is used to indicate a condition exists in the drive that could cause improper writing on the disk. When this line is true, further writing is inhibited, as are other drive functions, until the condition is corrected.

This condition is caused by either the +12 volt or +5 volt supply dropping below the specified limits, and on power up until a successful recalibration sequence is completed.

INDEX

The Index signal is provided once each revolution, 16.7 milliseconds nominal, to indicate the beginning of the track. Normally, this signal is false and makes the transition to true to indicate Index. Only the transition from logical false to logical true is valid.

READY

When true, the Ready signal, together with Seek Complete, indicates that the drive is ready to read, write or seek, and the I/O signals are valid. When this line is false, all controller-initiated functions are inhibited.

The typical time after power on for Ready to be true is fifteen seconds. Track 0, Seek Complete, and Ready come true sequentially during power on.

SELECT STATUS

A Status line is provided at the J2/P2 connector to inform the host system of the selection status of the drive.

The Drive Selected line is driven by a TTL open collector drive (see Figure 3-7). This signal goes active only when the drive is programmed as Drive X, X = 0, 1, 2, or 3, by programming the shunt on the drive, and the Drive Select X line at J1/P1 is activated by the host system.

DATA TRANSFER SIGNALS

All lines associated with the transfer of data between the drive and the host system are differential in nature and may not be multiplexed. These lines are provided at the J2/P2 connector on all drives.

Signal level are defined by RS-422A. Two pairs of balanced lines are used for the transfer of data: MFM Write Data and MFM Read Data. Figure 3-10 illustrates the driver/receiver combination used with the drive for data transfer signals.



FIGURE 3-10 DATA TRANSFER LINE DRIVER RECEIVER

MFM WRITE DATA

This is a differential pair of lines that define the flux transition to be written on the track. The transition of the + MFM Write Data line going more positive than the - MFM Write Data line causes a flux reversal on the track if Write Gate is active. This signal must be driven to an inactive state, + MFM Write Data more negative than - MFM Write Data, by the host system when in a read mode.

The delay from the leading edge of Write Gate to the Write Data pulse is 400 nanoseconds, maximum.

Host controllers may implement write precompensation circuits that recognize worst case patterns and adjust the write data waveform. Although a value cannot be specified for write precompensation, Tandon suggests a value of 12 nanoseconds for systems using MFM double density recording format starting at cyclinder 128.

MFM READ DATA

The data recovered by reading a prerecorded track is transmitted to the host system via the differential pair of MFM Read Data lines. The transition of the + MFM Read Data line going more positive than the - MFM Read Data line represents a flux reversal on the track of the selected head.

3.6 DRIVE ADDRESS AND OPTION SELECTION

The drive address and option selection is determined by the programmable jumper blocks located on the logic circuit board. If jumper configurations are changed, power should be cycled off and on, so that the microprocessor can recognize the new configuration.

The option programming guide is contained in Table 3-3.

3.7 SHIPPING PACK AND HANDLING

Figures 3-11 through 3-13 provide basic information on recommended design guidelines for packaging systems.

From various drop tests conducted, it has been established that drives subjected to shock loads in excess of forty G's may be damaged and consequently not meet published performance specifications for data reliability, margins, and function.

In order to avoid media or head damage, it is recommended that:

- 1. Drive mounting designs incorporate some type of shock dampening consideration.
- 2. Shipping cartons protect the drive within the system to withstand forty G's.
- 3. Individual drives are handled carefully, e.g., receiving and in-process personnel are property trained, surface mats are used on working surfaces to prevent the possibility of "handling shock," and padding is placed on racks and carts.

Please emphasize the critical aspects of handling these drives to all concerned people. In addition, Tandon provides technical assistance on packing and handling to customers upon request.

	Table 3-3 OPTION PROGRAMMING GUIDE					
Option Jumper	Function	Factory Programmed, User Accessible	Usage			
W15	Drive Select	I	Omit W15 for drive-to-controller lines, enabled when drive not selected.			
W16	Index	I	Omit W16 for index-to-controller line, enabled when drive not selected.			
RTW7	Read Terminator	I	Install only at the end drive for daisy chain data.			
WTW8	Write Terminator	I	Install in all drives for radial data.			
S4W9	Drive Select 4	ο				
S3W10	Drive Select 3	0	Install one of four plugs only. Plug corresponds to drive address			
S2W11	Drive Select 2	о (
S1W12	Drive Select 1	I)				
U19	Terminator Pack	Ι	Install at the end drive only for daisy chain control lines.			

NOTES:

O = Omit I = Install



ENCLOSE UNIT IN POLY BAG TO AVOID SURFACE SCRATCHES AND OTHER DAMAGE.

STEP B.

MOLD POLYURETHANE "CLAMSHELL" TO "CUBE" UNIT.

FIGURE 3-11 RECOMMENDED SHIPPING PACK DESIGN, CONFIGURATION 1



CONFIGURATION 1



FIGURE 3-12 RECOMMENDED SHIPPING PACK DESIGN, CONFIGURATION 2



ENCLOSE UNIT IN POLY BAG TO AVOID SURFACE SCRATCHES AND OTHER DAMAGE.



FIGURE 3-13 RECOMMENDED SHIPPING PACK DESIGN, CONFIGURATION 3



FIGURE 3-13 (CONTINUED) RECOMMENDED SHIPPING PACK DESIGN, CONFIGURATION 3



LOAD FACTOR **

NOTES

THE GRAPH GIVES FOAM THICKNESS TO SATISFY 10 G MINIMUM SHOCK LOADS ON STANDARD 30 INCH DROP TEST. HENCE, ALL PACKAGE DESIGNS SHOULD BE TESTED TO VERIFY THEIR ULTIMATE PERFORMANCE.

* THICKNESS IS IN INCHES. ** THE LOAD FACTOR IS IN LBS. PER SQ. INCH. THE FORMULA IS: LOAD FACTOR = UNIT'S WEIGHT (LBS.) SMALLEST SIDE OF UNIT (SQ. INCHES)

> FIGURE 3-14 LOAD GRAPH

Tandon CORPORATE OFFICES

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